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# EXTENDING RDMA FOR ALTERNATE FABRICS

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# INTEL OMNI-PATH<sup>®</sup> ARCHITECTURE (OPA) VERBS COMPATIBILITY

# NEW FABRIC, LEGACY APPLICATIONS

**It turns out it is possible to support new fabric features and legacy applications without changing the APIs**

**But...**

**We can do better**

# NEW FABRIC FEATURES

- **Per VL MTU**
- **Extended MTU**
  - 8K, “10K”
- **New advanced QoS support**
  - Preemption
  - More advanced buffering
  - Advanced topology support
  - Extended SLs
- **Management**
  - 2K MADs

# LEGACY VERBS APPLICATIONS

## 3 “classes” of Verbs applications

### 1. Use rdmacm

- Path Record query is hidden
- Query for Path Records but treat data as opaque

### 2. Query for Path Records but interpret data from the Path Record

### 3. Don't query for Path Records

# LEGACY VERBS APPLICATIONS

## 1. Use rdmacm

- Subclass:
  - Query for Path Records but treat data as opaque (IPoIB, SRP, etc)
- These are the most flexible application
- Already leverage existing infrastructure which abstracts fabric details

# LEGACY VERBS APPLICATIONS

## 2. Query for Path Records but interpret data from the Path Record

- Caution must be used

# LEGACY VERBS APPLICATIONS

## 3. Don't query for Path Records

- Make assumptions about the fabric which may not be true
- Technically these are not IB compliant!!!
- Work with configuration constraints on both IB and OPA



# EXISTING VERBS INTERFACE

## OPA leverages existing verbs fields by emulating an InfiniBand device

- **Per VL MTU**
  - SL obtained from Path Record
  - SL to VL details kept specific to the hfi1 driver layer
- **Extended MTU**
  - Obtained from Path Record
  - MTU enums are a “natural extension” of the IBTA defined values
- **New QoS support in hardware**
  - SL Obtained from Path Record
  - SL to VL and VL to SL details are contained in the driver
  - SLs are preserved end to end through new mapping tables
  - Extended SLs
    - Verbs is limited to the original 16

# MANAGEMENT

- **New scalable MADs**
  - 2K in size
  - Aggregates
- **SMP class version (different name space for OPA)**
  - Different configuration requirements
    - OpenSM will not work
  - Applications no longer require direct access to the fabric
- **InfiniBand GSI MADs are still supported**
  - Still 256 bytes (same class versions)
  - CM (rdmacm)
  - SA
    - Local SA cache of SA data through ibacm
    - Full rdmacm support

# VERIFIED ULPS

**... and the following work without modifications**

- IPoIB
- SRP
- iSER
- NFSoRDMA
- Lustre
- perftest benchmarks
- MPIs



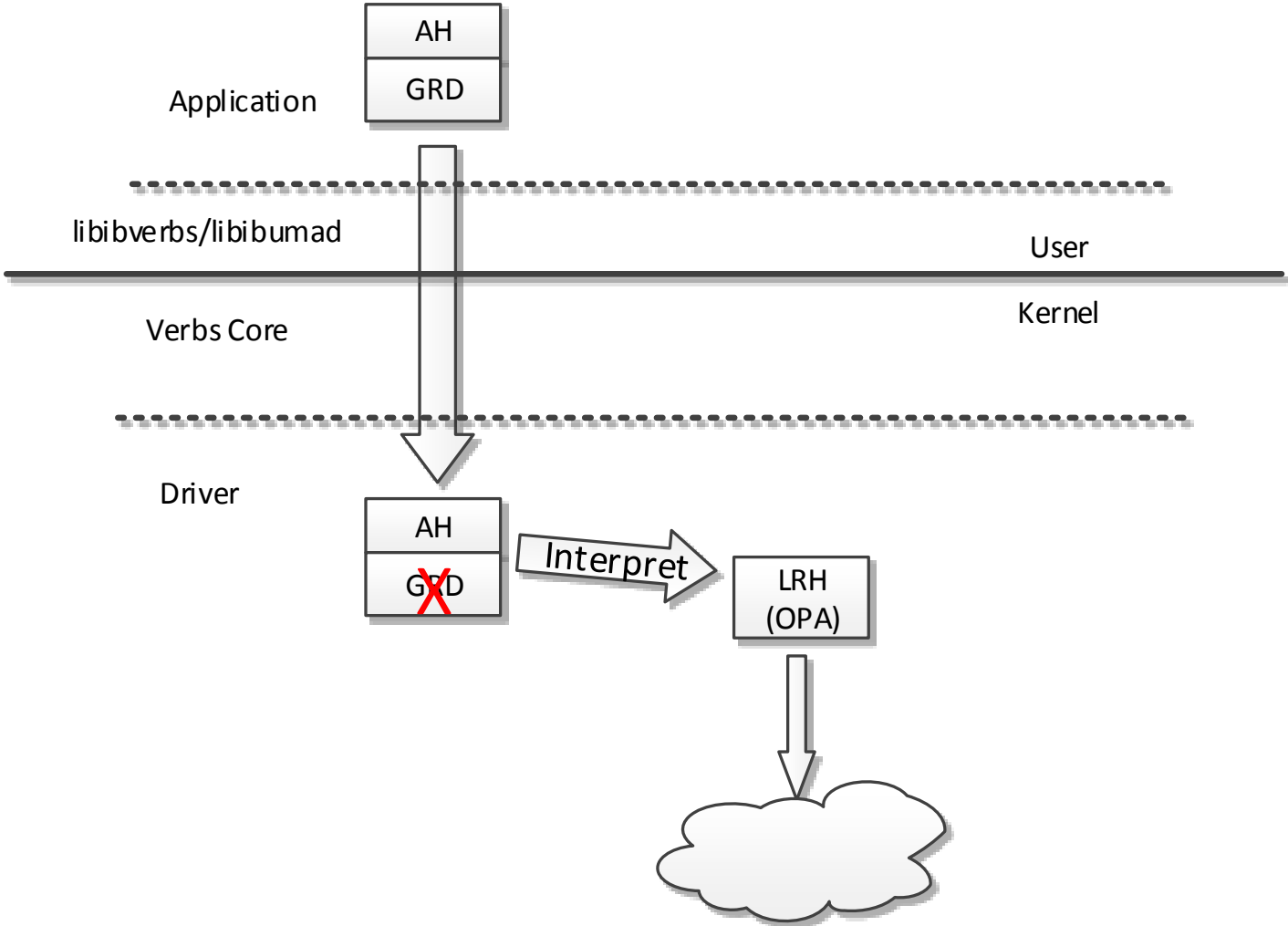
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# OPA EXTENDED LIDS

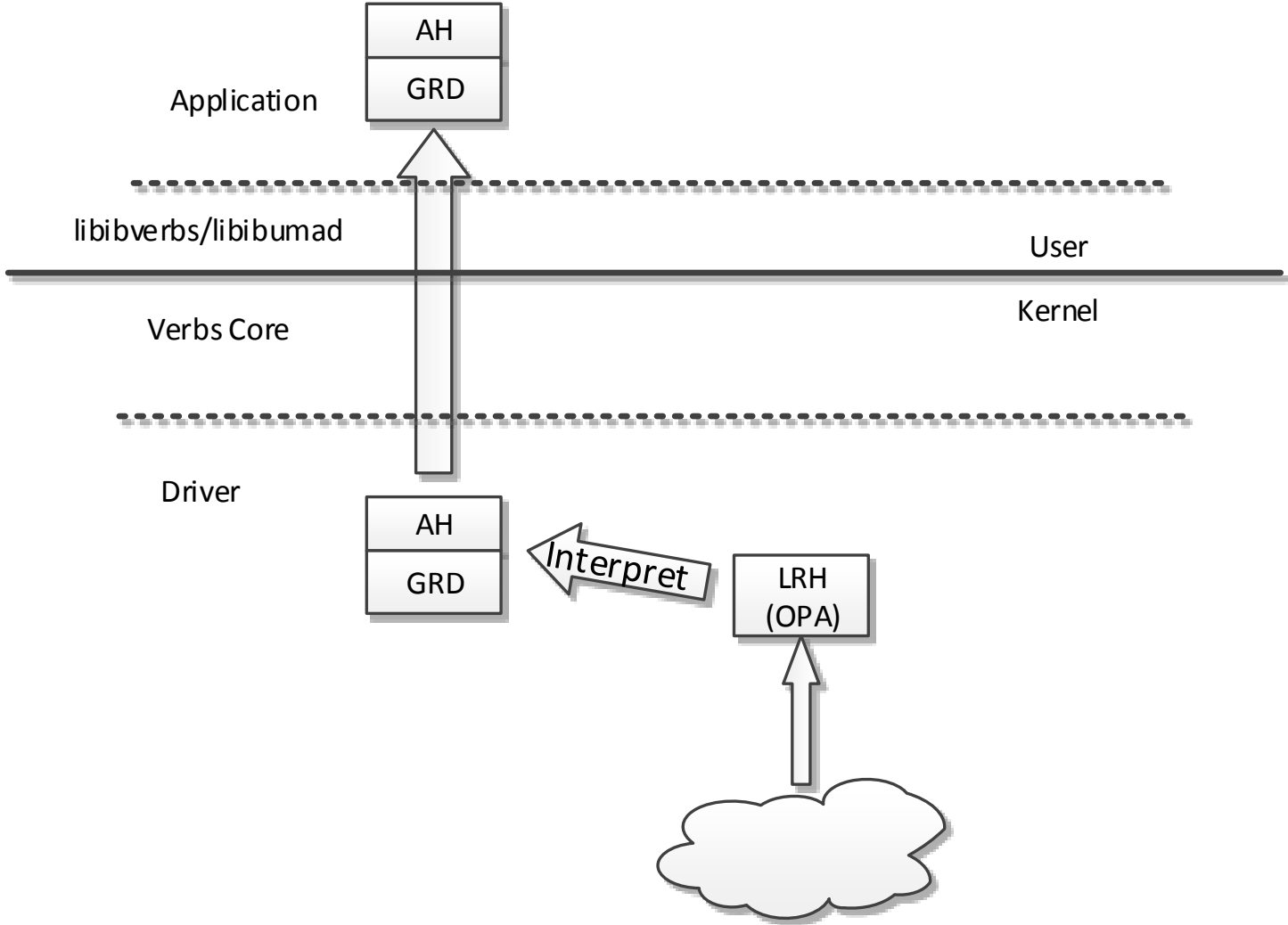
# SUPPORTING 32 BIT LIDS

- **OPA LIDs can be up to 32 bits**
  - Rdma stack only supports 16bit LIDs
- **Requirements**
  - Support extended LIDs only on fabrics which require them
  - Minimal to no application changes
- **Architecture**
  - Leverage existing alternate addressing schemes to pass LID data through the verbs stack
  - Keep changes for OPA within OPA specific code as much as possible

# APPLICATION SEND



# APPLICATION RECV



# OPA VERBS APPLICATIONS

- **Most applications already work with OPA**
  - Especially if they work with RoCE
  - A simple audit may be needed
  - Some applications may need to be called with alternate input
    - `ib_write_bw` for example requires a GID index (similar to RoCE)
- **Some standard `ibv_*` calls have limitations**
  - `ibv_query_port` can't return a valid LID or SM LID
  - Applications are not required to use those
  - We require new verbs
- **SM LID is available via `sysfs`**
  - Is only required for management applications
  - The `ibacm` daemon is the official SA cache for OPA
- **RoCE Applications have the similar limitations**





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# GOING FORWARD

# VERBS LIMITATIONS

- **MTU is IB specific**
  - Affects RoCE, Usnic, and now OPA
- **Heck pretty much every Verb and data structure is IB specific**
- **Link Layer is required by software for functionality**
  - Bring the new kernel immutable data to user space
  - Remove necessity for Link Layer as an application input
  
- **Take a more object oriented approach to the interface**
  - Borrow ideas from libfabric/rdmacm
  - Use opaque data structures
  - Use more generic data structures
- **New interfaces which are not InfiniBand specific**

# FUTURE MANAGEMENT

## ▪ Remove management from applications

- opensm-libs required by openmpi... ☹
- Leverage ibacm/librdmacm

## ▪ Enhance management for scalability

- Enhance MAD timeout mechanisms
  - RMPP total transfer
- More efficient processing of queues
  - Threads per device
- General clean up
  - New tracing mechanism

## ▪ Enhance management interfaces to be fabric agnostic

# CALL TO ACTION

- **Have to emulate a verbs device now**
  - Why?!?!?!?!?!?
  - USNIC, Gave up and are now a libfabric user...
- **Take a more object oriented approach**
  - What is a QP?
  - What is an address (address handle)?
- **User space has libfabric, what does the kernel have?**

**Apps should be agnostic to the fabric...  
But the interface has to be agnostic first.**

**Make a “verbs easy” button**

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**THANK YOU**

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