**OFI WG Bi-Weekly telecom – 06/06/2017**

**Agenda:**

* Update on current release schedule on rev 1.5
* Feature Request / multirail
* HOTI tutorial update

**Update on current release schedule**

* How close do folks think we are to being ready for RC?
* USNic, GNI provider has pending work, probably another week or two
* BlueGene is ready
* PSM2 in good shape
* Network Direct not quite ready

**Feature Request – Multi-rail support**

* Multi-rail could be implemented either above or below the libfabric i/f.
* Generally, you would like this below the libfabric API in the provider, but some applications may want more control. For example, MPI may want to map instances of an interface to a particular thread.
* Could this be implemented in a provider, with finer-grained control exposed via vendor options? PSM might do something like this today via environment variables (but not sure).
* Objective for now to gather some requirements – is it for bandwidth, or flexibility? How many rails may be needed, and how are they used?
* Knee-jerk thoughts?
	+ MPICH – tempting to leave it below the API, but can easily see requirements from the application layer for more control
	+ Could think about this in the same light as the utility providers – kind of a plug-in module that speaks libfabric on the bottom edge.
	+ In at least some scenarios, it’s about improving bandwidth, both improving b/w for a given message, and allowing different messages to take different paths.
	+ It seems as though Daniel (? original poster) was pointing at a requirement for non-homogeneous systems, i.e. one where a given port ID isn’t always connected to the same network on all nodes. This may lead us to discussion of exposing topology information (something we haven’t addressed in the past).
* From a b/w perspective, is this about delivering increased b/w to any given rank? Yes – the idea is if you assign multi-rail to a given rank, you improve the b/w available to that rank.
* May need to expose topology information.

**HOTI Tutorial**

* Sean has submitted a tutorial and received strong acceptance, based on the SC-15 tutorial.
* Feedback was to include other i/fs than just MPI or SHMEM, other than so-called HPC use cases.
* Anybody have any experience? HPX? UPC++?
* Sung to try to dig up some contacts to help. May also be willing to help with the presentation – need to check on the dates.

**Recording:**

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| **OFIWG/libfabric meeting-20170606 1604-1** |
| Tuesday, June 6, 2017 |
| 12:04 pm  |  Eastern Daylight Time (New York, GMT-04:00)

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| [**Play recording**](https://cisco.webex.com/ciscosales/lsr.php?RCID=a6ceedc10c944fffa51861bd0ac971a8) (31 min 9 sec) |
| Recording password: aSmeRdS3 |

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**Webex link:** See the OFA central calendar for meeting logistics. <https://openfabrics.org/index.php/ofa-calendar.html>

**OFIWG Download Site:** [www.openfabrics.org/downloads/OFIWG](http://www.openfabrics.org/downloads/OFIWG)

**Github:** <https://github.com/ofiwg/libfabric>

**OFI Software Download Site:** [www.openfabrics.org/downloads/OFI](http://www.openfabrics.org/downloads/OFIWG)

**Next regular telecon**

Next meeting: Tuesday, 6/20/17

9am – 10am Pacific daylight time